Abstract of the Disclosure

In a channel estimating circuit, a Sync Word is demodulated by using a channel estimation value not with

5   a symbol or a slot including the Sync Word to be demodulated, but with its preceding and succeeding symbols or slots. Furthermore, in a channel estimation process, if a Synch Word to be demodulated is included in any of groups into which a plurality of symbols are

10   divided, the Sync Word is demodulated by using a channel estimation value obtained with the channel estimation process using not this group but another group.

15